

PATENT

Atty. Docket No.: 30320/37779

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Rose et al.

Serial No: 10/027,053

Filed: December 20, 2001

) Title: Bit Error Rate Test System
) for Multi-Source Agreement
) Compliant Transceivers

) Group Art Unit: 2133

) Examiner: Unknown

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Attention: Official Draftsman

Sir:

Enclosed herewith are four (4) sheets of formal drawings for substitution
in the above-referenced application.

Respectfully submitted,

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By:



Mark G. Hanley
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CERTIFICATE OF MAILING (37 CFR 1.8)

I hereby certify that this paper and the documents referred to as enclosed
therewith are being deposited with the United States Postal Service as first class
mail, postage prepaid, on **February 6, 2002**, in an envelope addressed to the
Commissioner for Patents, Washington, D.C. 20231.


Mark G. Hanley